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```
m128i line0 = _mm_shuffle_epi32(line0, _MM_SHUFFLE(0, 0, 0, 0)); //32bit version of same
instruction m128i line1 = _mm_shuffle_epi32(line1, _MM_SHUFFLE(0, 0, 0, 0)); m128i line2 =
_mm_shuffle_epi32(line2, _MM_SHUFFLE(0, 0, 0, 0)); line0= _mm_shuffle_epi32(line1,
_MM_SHUFFLE(1, 1, 1, 1)); //32bit version of same instruction line2= _mm_shuffle_epi32(line2,
_MM_SHUFFLE(1, 1, 1, 1)); m128i line3 = _mm_xor_si128(line1, line2); m128i line4 =
_mm_xor_si128(line0, line2); m128i line5 = _mm_xor_si128(line1, line0); m128i line6 =
_mm_xor_si128(line1, line1); m128i line7 = _mm_shuffle_epi32(line4, _MM_SHUFFLE(2, 2, 2, 2));
m128i line8 = _mm_shuffle_epi32(line5, _MM_SHUFFLE(2, 2, 2, 2)); m128i line9 =
_mm_shuffle_epi32(line6, _MM_SHUFFLE(2, 2, 2, 2)); m128i line10 = _mm_shuffle_epi32(line7,
_MM_SHUFFLE(2, 2, 2, 2)); m128i line11 = _mm_shuffle_epi32(line8, _MM_SHUFFLE(2, 2, 2, 2));
m128i
```

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